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Application

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September 12, 2002

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~~Pat. Assignment~~
CG-2, 1213 Jefferson Davis Hwy., Suite 320
Washington, DC 20231

Re: Application No.: 09/811,915
Filing Date: 03/19/01
IBM Docket No.: BUR920000082US1

Dear Sir:

Please correct the spelling of the name of the first inventor on the filing receipt for the above reference new patent application.

Should be : Wagdi

Thank you.

Sincerely,

Pat Blair
Administrative Assistant to Robert Walsh

Enclosure

OFFICE OF PUBLIC RECORDS
2002 SEP 17 PM 1:42
FINANCE SECTION



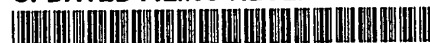
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/811,915	03/19/2001	2858	1250	BUR920000082US1 (13647)	7	50	2

CONFIRMATION NO. 8953

UPDATED FILING RECEIPT



OC000000006318697

Richard L. Catania, Esq.
Scully, Scott, Murphy & Presser
400 Garden City Plaza
Garden City, NY 11530



Date Mailed: 07/19/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Wadgi W. Abadeer, Jericho, VT;
William T. Motsiff, Essex Junction, VT;
Edward J. Nowak, Essex Junction, VT;

Assignment For Published Patent Application

INTERNATIONAL BUSINESS MACHINES CORPORATION, ARMONK, NY;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 05/02/2001

Projected Publication Date: 09/19/2002

Non-Publication Request: No

Early Publication Request: No

Title

Wafer level system for producing burn-in/screen, and reliability evaluations to be performed on all

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